



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Inventor: Alexander KOLBECK et al. **Attorney No.:** KOLB3002/JEK/JJC

Application No.: 09/926,792

Customer No.: 23364

Filed: April 3, 2003

Confirmation No: 8670

Examiner: Stephen M. D'Agosta

Art Unit: 2683

For: METHOD FOR OPERATING A PORTABLE DATA CARRIER
CONFIGURED FOR EXECUTING RELOADABLE FUNCTIONAL
PROGRAMS

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

INTRODUCTORY COMMENTS

Pursuant to Rule 37 C.F.R. §§ 1.56, 1.97 and 1.98, this Information Disclosure Statement is submitted in the above-identified patent application. A listing of documents to be published on the face of any patent granted from this application is submitted on Form PTO-1449. Any other documents or information submitted for consideration by the Examiner are listed in this paper. A copy of each patent listed on Form PTO 1449 is submitted herewith.

No fee is considered necessary since the references cited in the accompanying Form PTO 1449 are acknowledged as having already been received by the Office, as indicated in the 371 Formalities Letter dated February 1, 2002.

WO 98/09257, while listed in the International Search Report, is not cited in the form PTO-1449 since its U.S. counterpart, U.S. 5,923,884 is already cited by the Examiner in the Office action dated May 26, 2005.

Additional Documents or Information Not Listed on
Accompanying Form PTO-1449 Submitted for Consideration

International search report of PCT application PCT/EP00/05795 dated October 30, 2000. As acknowledged in the Office action dated May 26, 2005 in the pending U.S. application a copy of the search report has already been received by the Office and the references cited in the search report are included therewith.

Concise Statement of Relevancy
(Non-English Language Documents Only)

All of the non-English language patent documents listed on the Form PTO 1449 are already provided with a category of relevancy in the search report. Nonetheless, provided below is a brief statement as to relevancy of DE 197 23 676 A1.

DE 197 23 676 discloses a method for loading program applications to a chip card after the chip card has been handed out to the user. The teaching's central principle is that programs to be loaded are distributed by the operating system of the chip card to available free physical addresses and/or physical segments in a memory of the chip card by means of logical addresses and/or logical segments. Based on this central issue, two variants are provided.

According to the first variant, the operating system puts the applications to be loaded to physical start addresses. A table of the physical start addresses is created and the addresses therein are managed by a memory management unit. The physical start addresses of the table are added to logical addresses by an address adder in order to obtain physical addresses, which are deposited in a logical address space.

According to the second variant, the programs to be loaded are put and/or linked to arbitrary segment limits and/or logical segments of the memory. The programs to be loaded are distributed by the operating system to the free physical segments and the logical segments are associated with the physical segments in a table by means of modifying the most significant address bits by the memory management unit.

Statement of Cumulative Information

While not specified in the search report, DE 197 18 115 has Canadian English language counterpart CA 2,275,931. This Canadian patent is listed on form PTO-1449.

Statement Regarding Translations

An English language translation of DE 197 23 676 is not available to applicants.

Statement of Timeliness

This Information Disclosure Statement is submitted after three months from (i) the filing date of the above-identified U.S. National Patent application, or (ii) after three months from entry into the U.S. National Stage of the above-identified International Application, or (iii) the date of entry into the U.S. National Stage of the International Application that has been assigned the above-identified U.S. Patent application number, whichever applies; and after the mailing date of the first Office Action on the merits of the above-identified application, but prior to issuance of any Final Action, Notice of Allowance, or Action closing prosecution sent in such application. The certification under 37 C.F.R. § 1.97(e) is submitted separately or below, or the fee required under 37 C.F.R. § 1.97(c) and § 1.17(p) is submitted herewith.

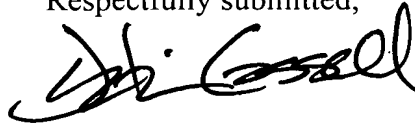
Application No.: 09/926,792
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Art Unit: 2683

If any issues remain that may be resolved by a telephone or facsimile communication with the Applicant's Attorney, the Examiner is invited to contact the undersigned at the numbers shown below.

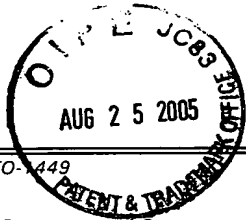
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Date: August 25, 2005

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Justin J. Cassell", written over a horizontal line.

JUSTIN J. CASSELL
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B/O Form PTO-1449 U.S. Department of Commerce Patent and Trademark Office Information Disclosure Statement by Applicant	Atty. Docket Number	Serial Number
	KOLB3002/JEK/JJC	09/926,792
	Applicant	
	Alexander KOLBECK et al.	
	Filing Date	Group
	April 3, 2003	2683

U.S. Patent Documents

Examiner Initial	Document Number	Date	Patentee/Applicant	Class	Subclass	Filing Date if Appropriate

Foreign Patent Documents

Examiner Initial	Ref.	Document Number	Publication Date	Country/Agency	Class	Subclass	Translation	
							Yes	No
	A1	2 275 931	Jul. 1998	Canada				
	A2	197 23 676 A1	Aug. 1998	Germany				X
	A3	0 949 595 A2	Oct. 1999	EPO				

Other Documents (Including Author, Title, Date, Pertinent Pages, Place of Publication, Etc.)

Examiner	Date Considered
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EXAMINER: Initial if citation is considered, whether or not citation is in conformance with MPEP 609; Draw a line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.